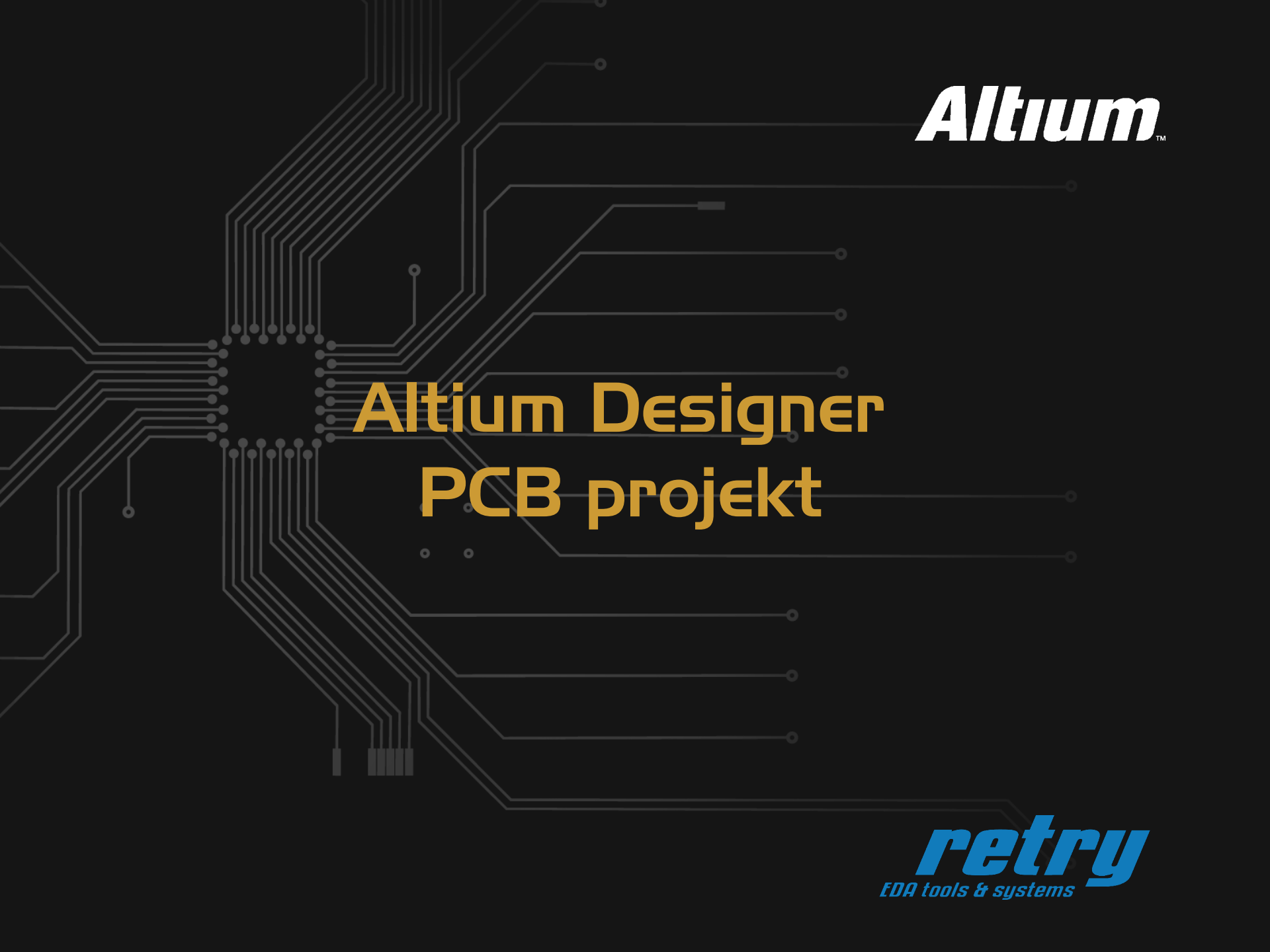


The background of the slide is a dark gray with a complex, light gray circuit board pattern. The pattern consists of numerous thin lines representing traces, some of which are bundled together, and small circles representing vias or pads. The pattern is more dense on the left side and becomes sparser towards the right.

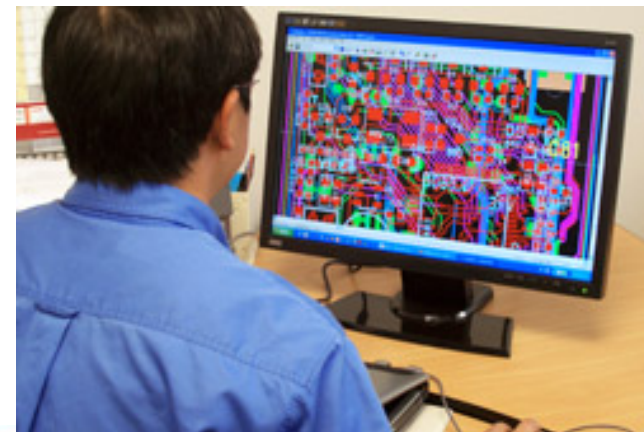
Altium[™]

Altium Designer PCB projekt

retry
EDA tools & systems

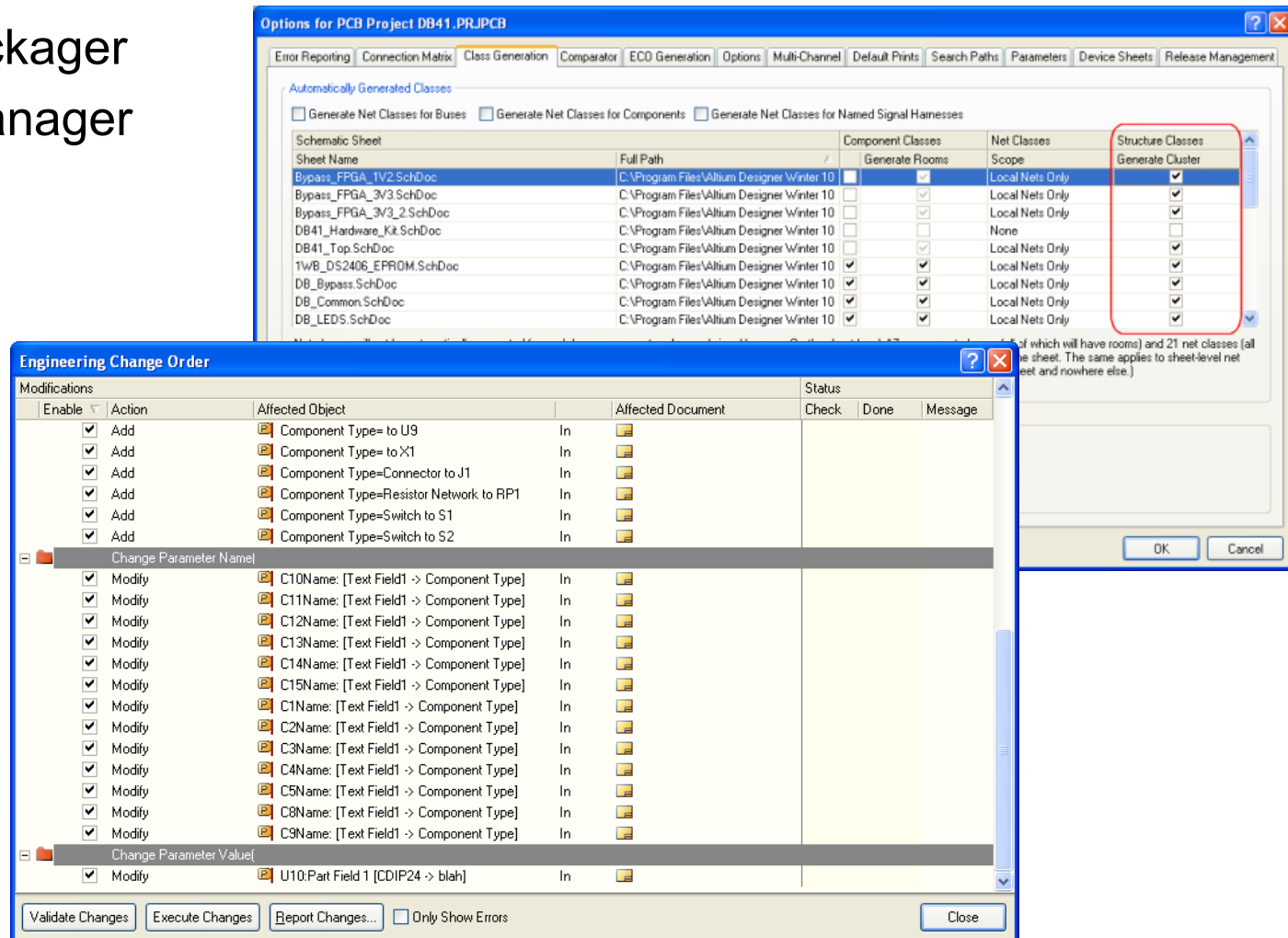
PCB projekt v AD

- Sdružuje informace o projektu
- Nastavení ERC
- Nastavení chybových hlášení
- Definování parametrů
- Výběr struktury projektu
- Šablony



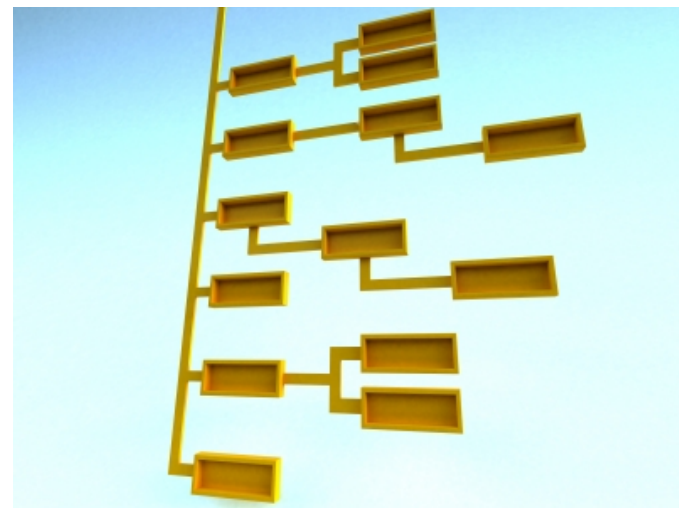
Užitečné nástroje projektů v AD

- Project Packager
- Storage Manager
- ECO



Doporučená adresářová struktura

- Adresář projektu
 - History
 - Soubory předchozích verzí dokumentů
 - Project Logs
 - Záznamy o úpravách projektu
 - Project Outputs
 - Základní adresář pro generované výstupy
 - **Sources** (dobrovolné)
 - **Libraries** (dobrovolné)
 - **3D models** (dobrovolné)
- *.Preview – zobrazovací cache
- *.Structure – kompilace projektu



Storage manager – ukládání bez obav

Storage Manager

AVRmodule.PrjPCB

Folders

- AVRmodule
 - AVRmodule1
 - Project Logs for AVRmodule
 - Project Outputs for AVRmodule

Files in Project

File	Size	Kind	Date Modified	Status
AVRmodule.Annotation	0	Annotation	23.4.2010 16:28:16	Version control disabled
AVRmodule.IntLib	24 576	IntLibrary	23.4.2010 19:21:08	Version control disabled
AVRmodule.PcbDoc	433 664	PCB	25.4.2010 16:18:36	Version control disabled
AVRmodule.PrjPCB	32 652	PcbProject	25.4.2010 16:18:38	Version control disabled
AVRmodule_Top.SchDoc	204 800	SCH	25.4.2010 16:18:38	Version control disabled

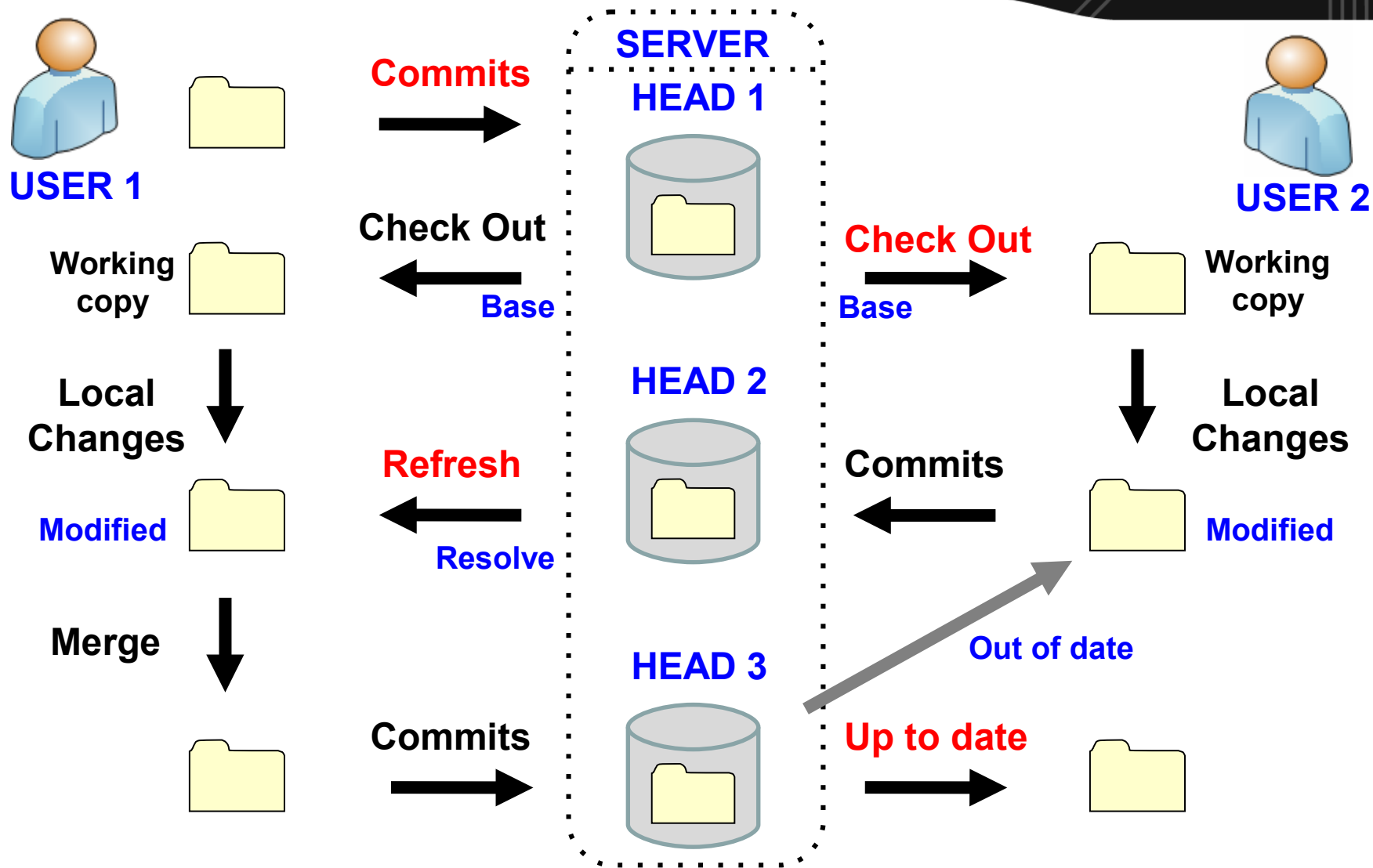
D:\Altium\AVRmodule ☒ Only Show Files in Current Project

Time line for AVRmodule_Top.SchDoc

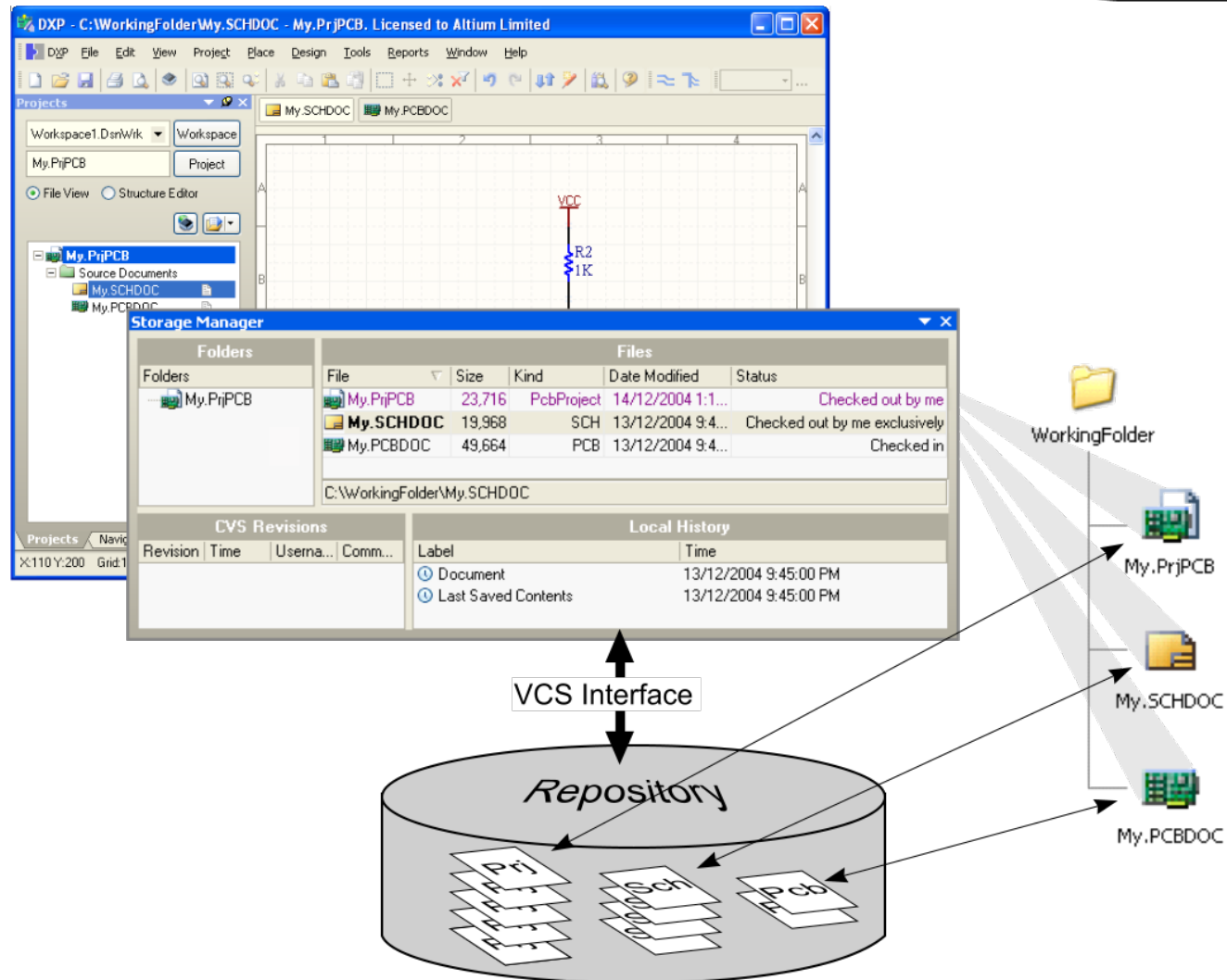
Event	Time	Version	Author	Comment
Modify document	21.6.2010 16:51:08	Modified open document	xtosov00	
Save to file	25.4.2010 16:18:36	Latest saved version	xtosov00	
Save to history	25.4.2010 16:18:36	Version 8	xtosov00	
Save to history	23.4.2010 18:55:28	Version 7	xtosov00	
Save to history	23.4.2010 14:46:19	Version 6	xtosov00	
Save to history	23.4.2010 14:42:22	Version 5	xtosov00	
Save to history	23.4.2010 14:40:34	Version 4	xtosov00	
Save to history	23.4.2010 14:36:55	Version 3	xtosov00	



Verzovací systémy



Verzování v praxi



Nový koncept řízení Lifecycle (R10)

Usnadnění řízení vývoje a označování verzí

Cílem je poskytnout potřebná data v průběhu celé produkce výrobku a i po ní s přesným záznamem postupu vývoje

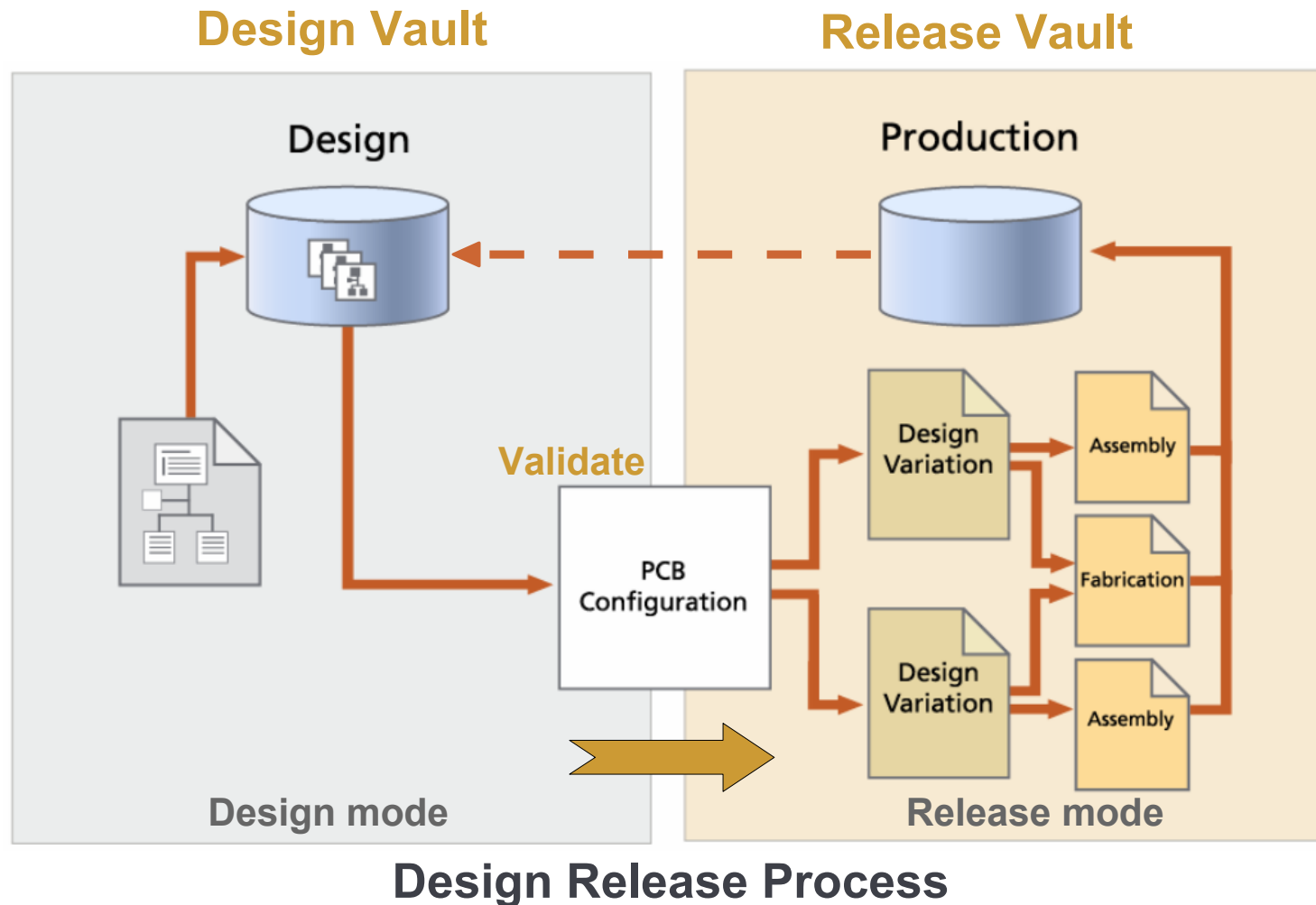
Statusy (Planned, In Design, In Production, Closed, Obsolete)

Definované přechody mezi stavy

Oprávnění pro změny statusu



Release proces



Vytvoření položky

Prázdný Item - Planned



Release proces

Configuration Manager for TEST_001.PrjPCB

**PCB Project: TEST_001**

PCB projects represent the design intent for a family of related manufactured boards. Projects are parametric, outputs are controlled by variants and the set of included output jobs.

**PCB Configurations**

PCB Configurations map a parametric PCB project to a real item that will be manufactured as a blank or assembled PCB.

[Add New Configuration...](#)

Configuration [Delete...](#)
☒ Ignore Variants - Include all components
☐ Link to Variant
Variants control which components to use

Include Output Jobs
☐ Assembly ☒ Bare
☒ Validation

Configuration [Delete...](#)
☒ Ignore Variants - Include all components
☐ Link to Variant
Variants control which components to use

☒ Assembly ☐ Bare
☒ Validation

Target Release Vault TEST_001_RV
Every PCB project is associated with a Target Release Vault. Individual Configurations are linked to Items in this Release Vault.

**Target Items in Release Vault**

Each configuration maps to an Item in the Release Vault. Each time a configuration is released, the data generated is stored in a new revision of the target Item – an Item Revision.

Target Item [Clear](#)
Last Release [01.A.1]
Released 17 Jun 2010
[Abandoned]

Target Planned Revision for Next Release

Target Item [Clear](#)
Unreleased

Target Planned Revision for Next Release

[More Information...](#)

Přiřazení projektu do položky



Release proces

PCB Release

MyExampleDesign
3 Configurations
Up To Date

PB02
2 Configurations
Not In VCS

Fabrication
D-810-2000
01.A.1 [Planned]

Production - Standard Version
D-820-2001
01.A.3 [New From Design]

Production - Deluxe Version
D-820-2002
01.A.1 [Planned]

Design Vault
Production - Deluxe Version of MyExampleDesign
MyExampleDesign is synchronized with version control
Projects which are synchronized with version control can be released.

☐ Design Mode
☒ Release Mode

Revision 01.A.1 of D-820-2002
Releasing Revision 01.A.1 of D-820-2002
Use the Commit Release button below to run the release process.

Release Vault

Checkout Snapshot Validate Design Generate Outputs Commit Release

File Name	Output Job	Output Medium	Output Name	Category	Source	Status
BOM						
Bill of Materials	Assembly.OutJob	Generate Files	Bill of Materials	Documentation	[Project]	Missing
PDF						
[D-820-2002-01.A.1] Assembly Drawings.PDF	Assembly.OutJob	Publish To PDF		Assembly		Missing
[D-820-2002-01.A.1] PCB and Schematic Prints.PDF	Documentation.OutJob	Publish To PDF		Documentation		Out Of Date
[D-820-2002-01.A.1] Validations.PDF	Validations.OutJob	Publish To PDF		Validation		Missing
Pick Place						
Generates pick and place files	Assembly.OutJob	Generate Files	Generates pick and place fi	Assembly	MyExamplePCB.PcbDoc	Missing
[D-820-2002-01.A.1] RawSystemBOM.xml				Assembly		Missing

Generate Open Delete

Output Folder: C:\Design - Working Copy\Products\Automotive\My Example Design\Production - Deluxe Version\

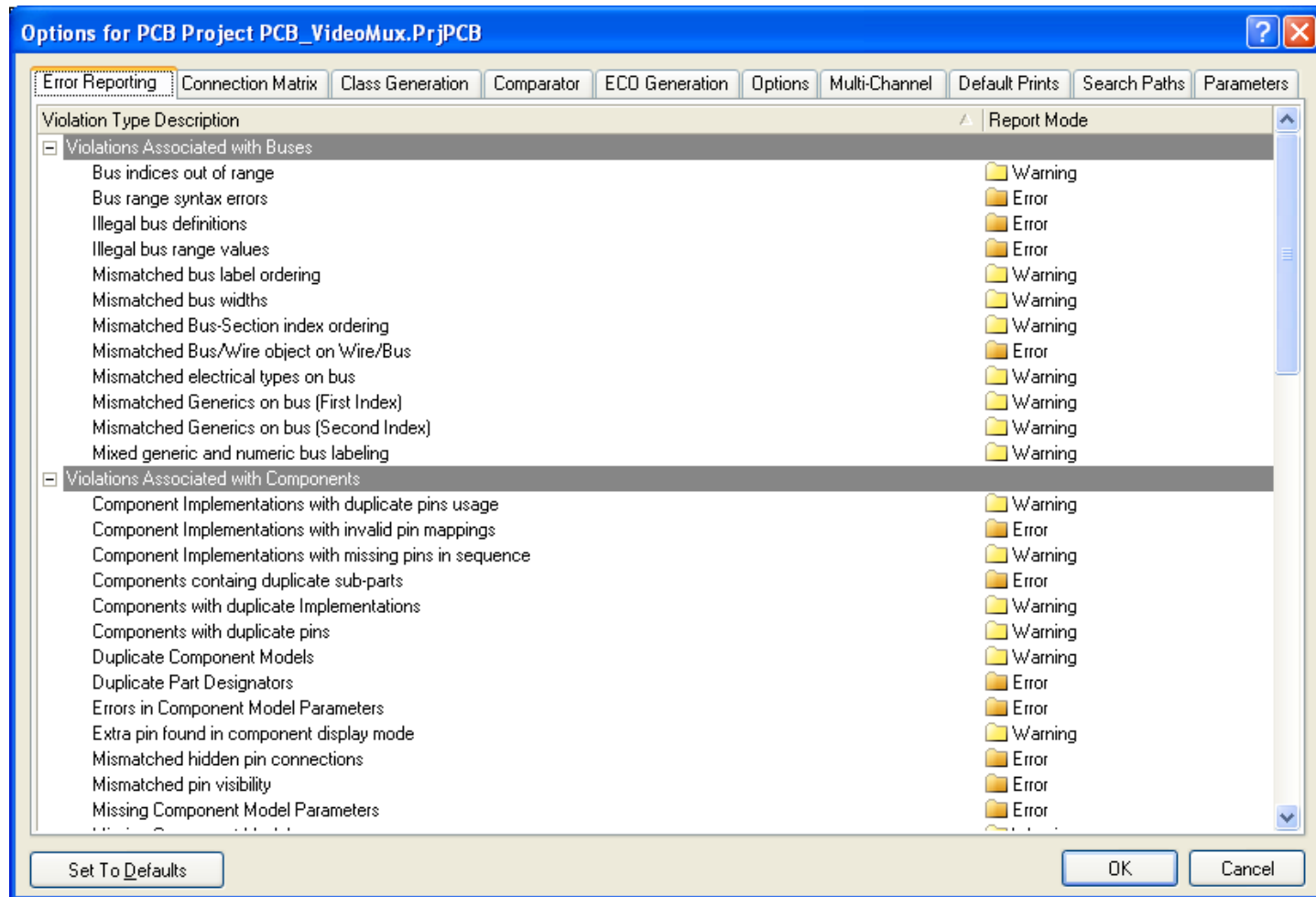
Položka včetně historie revizí



Výsledek

Model 01 New From Design		Model 02 Planned
Prototype 01.A New From Design	Prototype 01.B New From Design	Prototype 02.A Planned
Rev. 01.A.1 New From Design 09-Jun-10 15:03 Pending Prototype Approval 09-Jun-10 15:15 In Prototype 09-Jun-10 15:16 Closed Prototype 10-Jun-10 11:12 In Prototype 13-Jul-10 14:05 New From Design 13-Jul-10 14:05	Rev. 01.B.1 New From Design 09-Jun-10 15:20 Pending Prototype Approval 09-Jun-10 15:25 In Prototype 09-Jun-10 15:25 Pending Production Approval 09-Jun-10 15:25 In Production 09-Jun-10 15:25 Obsolete 13-Jul-10 11:24 In Production 13-Jul-10 14:05 In Prototype 13-Jul-10 14:09 New From Design 13-Jul-10 14:09	Rev. 02.A.1 New From Design 10-Jun-10 10:45 Rev. 02.A.2 New From Design 13-Jul-10 11:23 Abandoned 13-Jul-10 11:25 New From Design 13-Jul-10 11:25 Pending Prototype Approval 14-Jul-10 11:20 Rev. 02.A.3 Planned 14-Jul-10 11:20

Project Options



Altium Designer



Děkuji za vaši pozornost.